

FIG.1

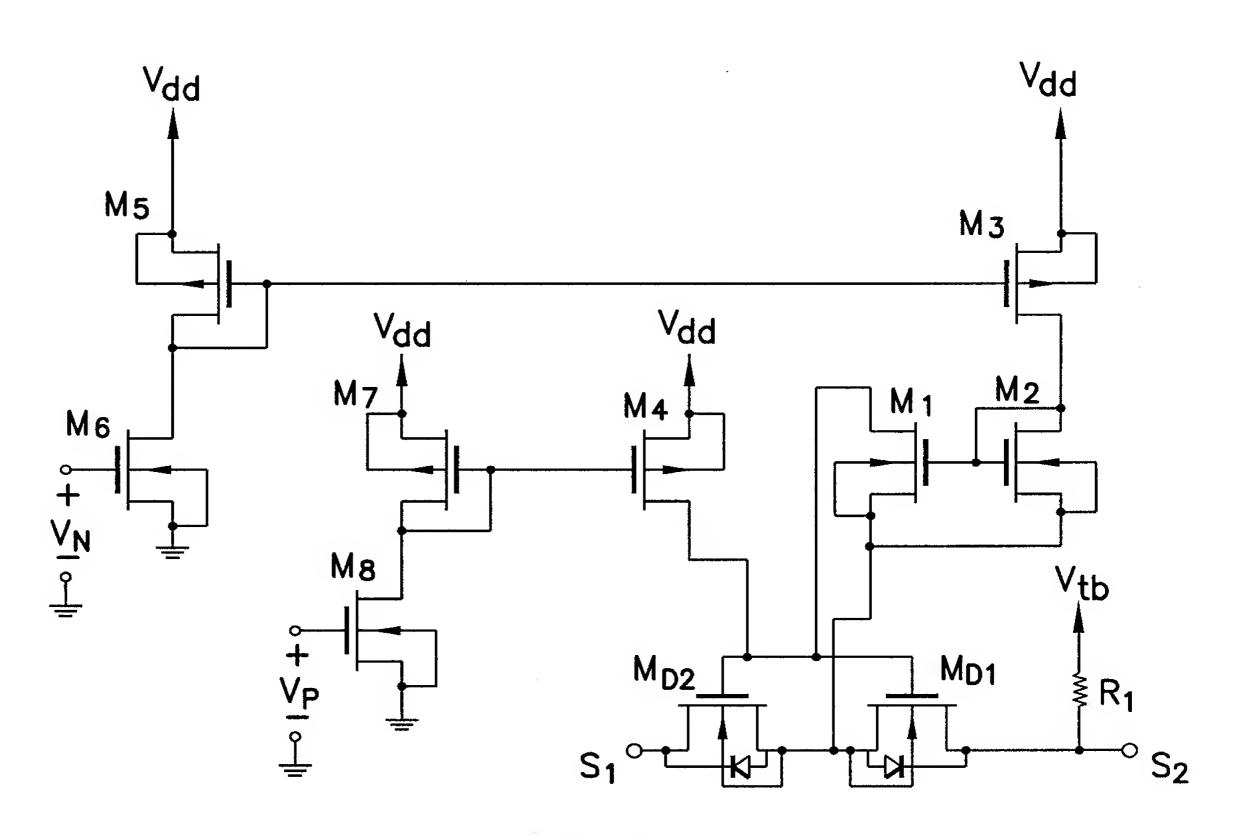


FIG.2

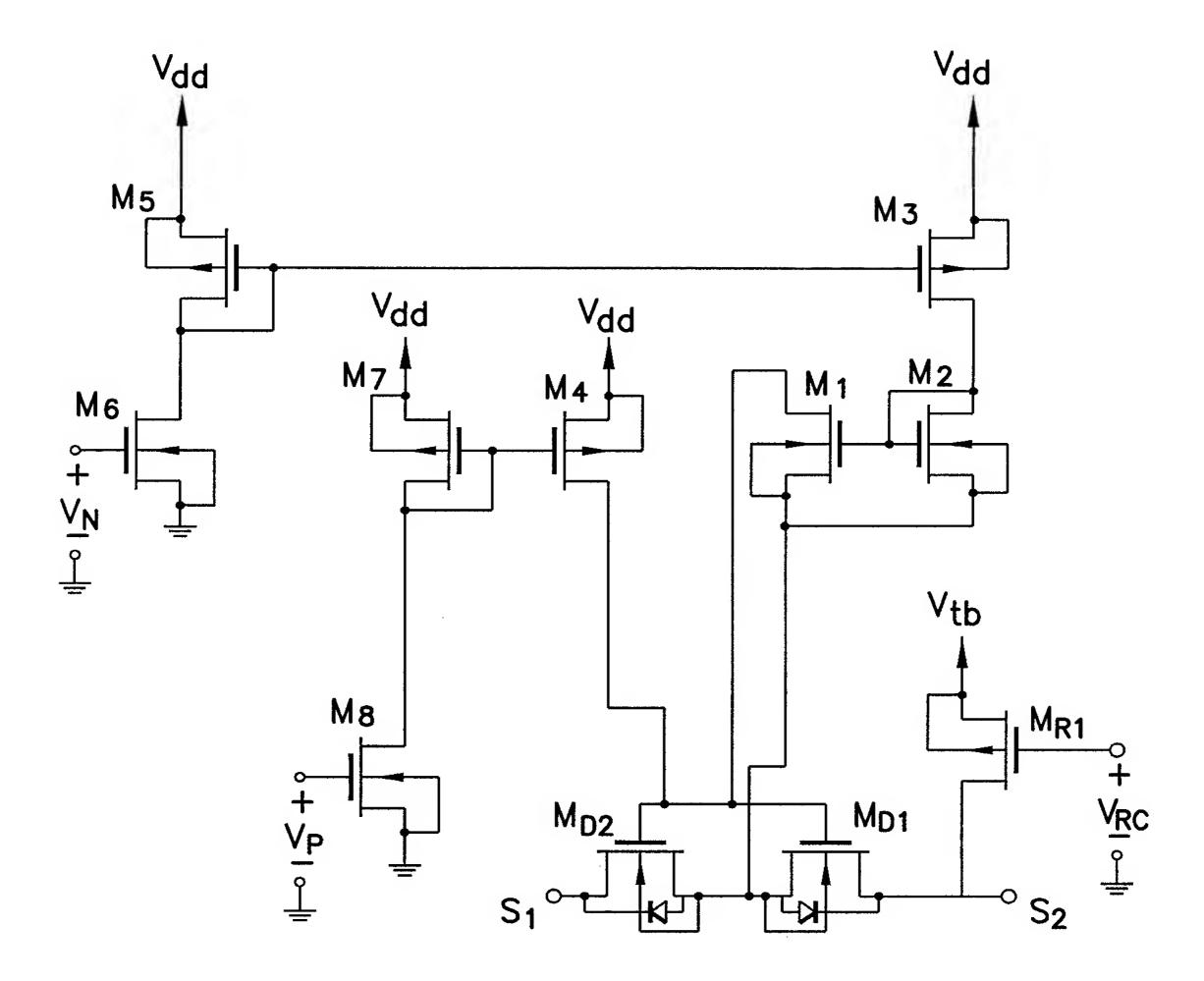
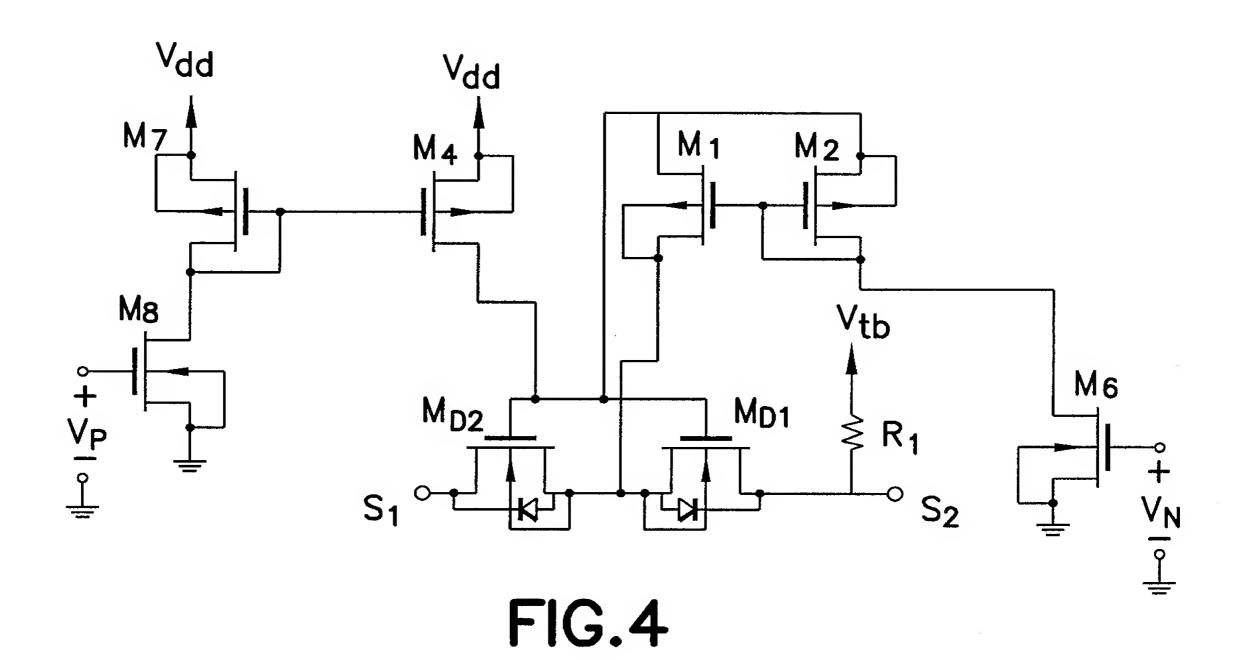


FIG.3



LEVEL SHIFTER VP CONTROL LOGIC Vdd Vbb S1 Vbb S2

FIG.5

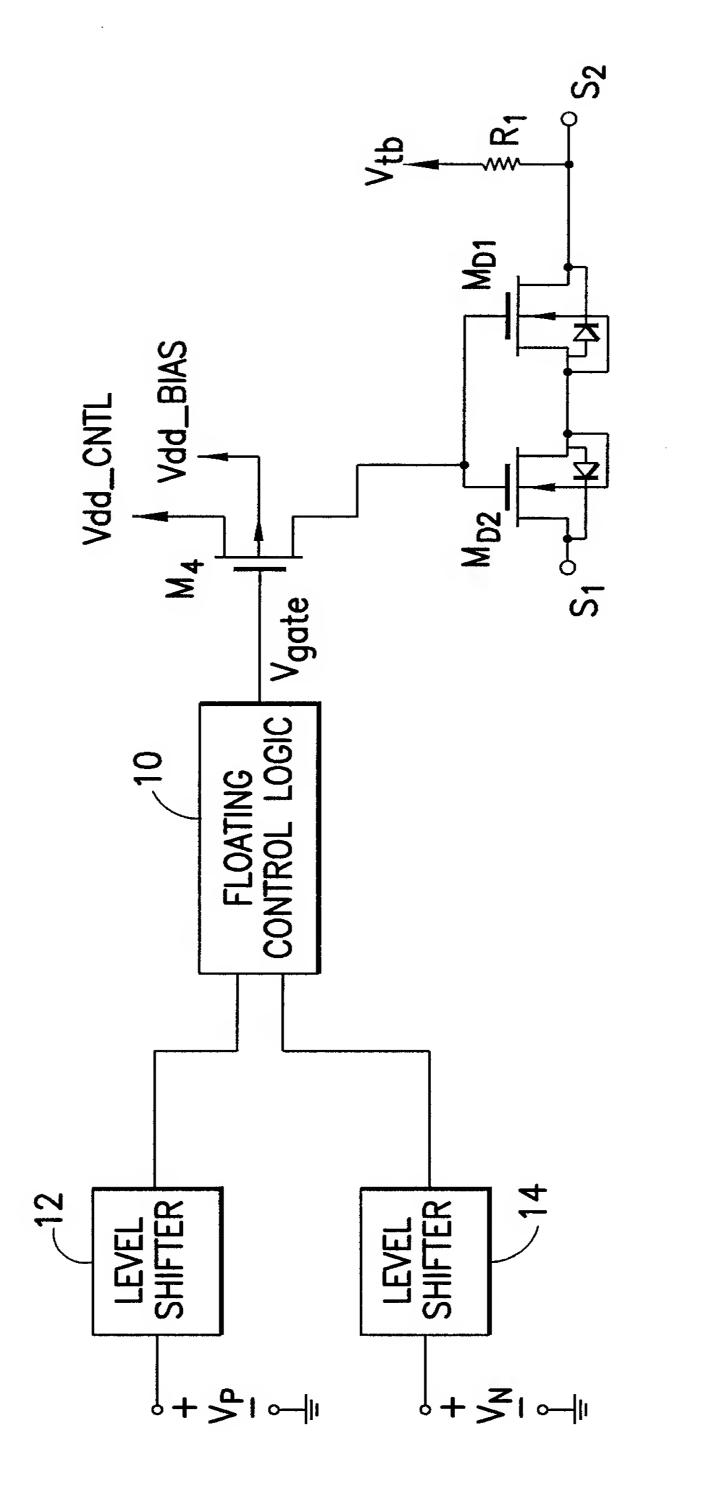
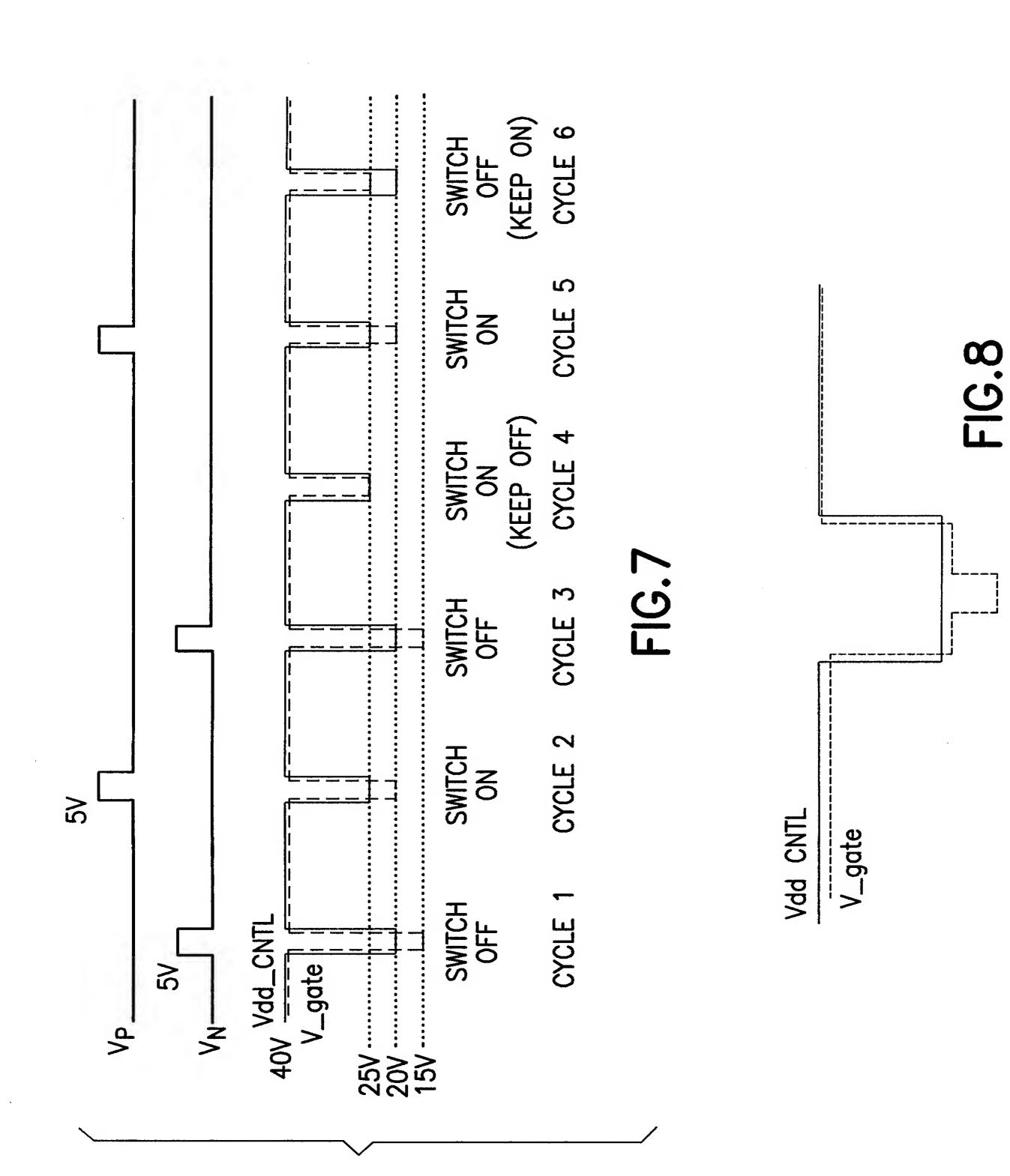


FIG.6



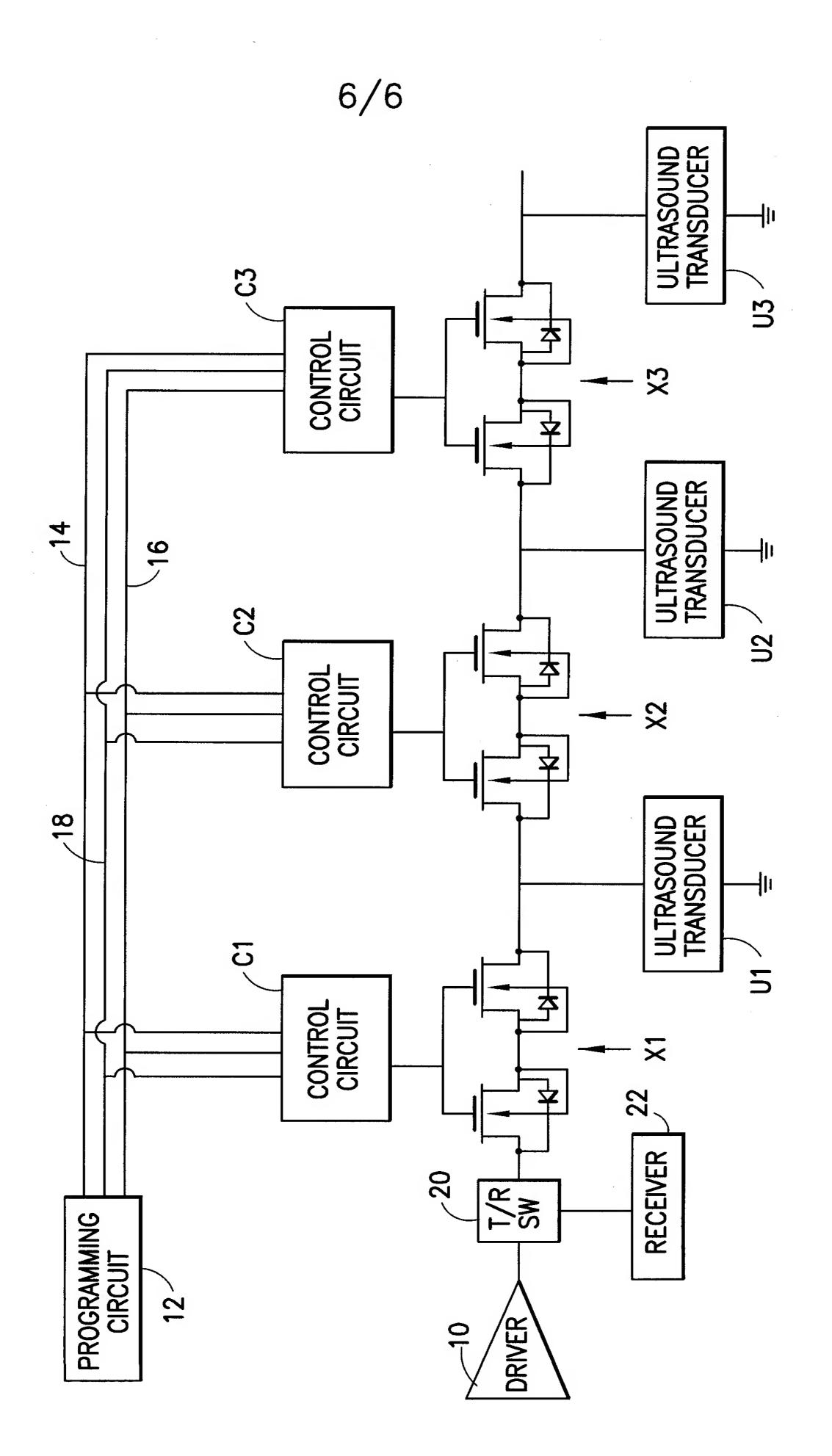


FIG.9